

FIG. 1 (Prior Art)

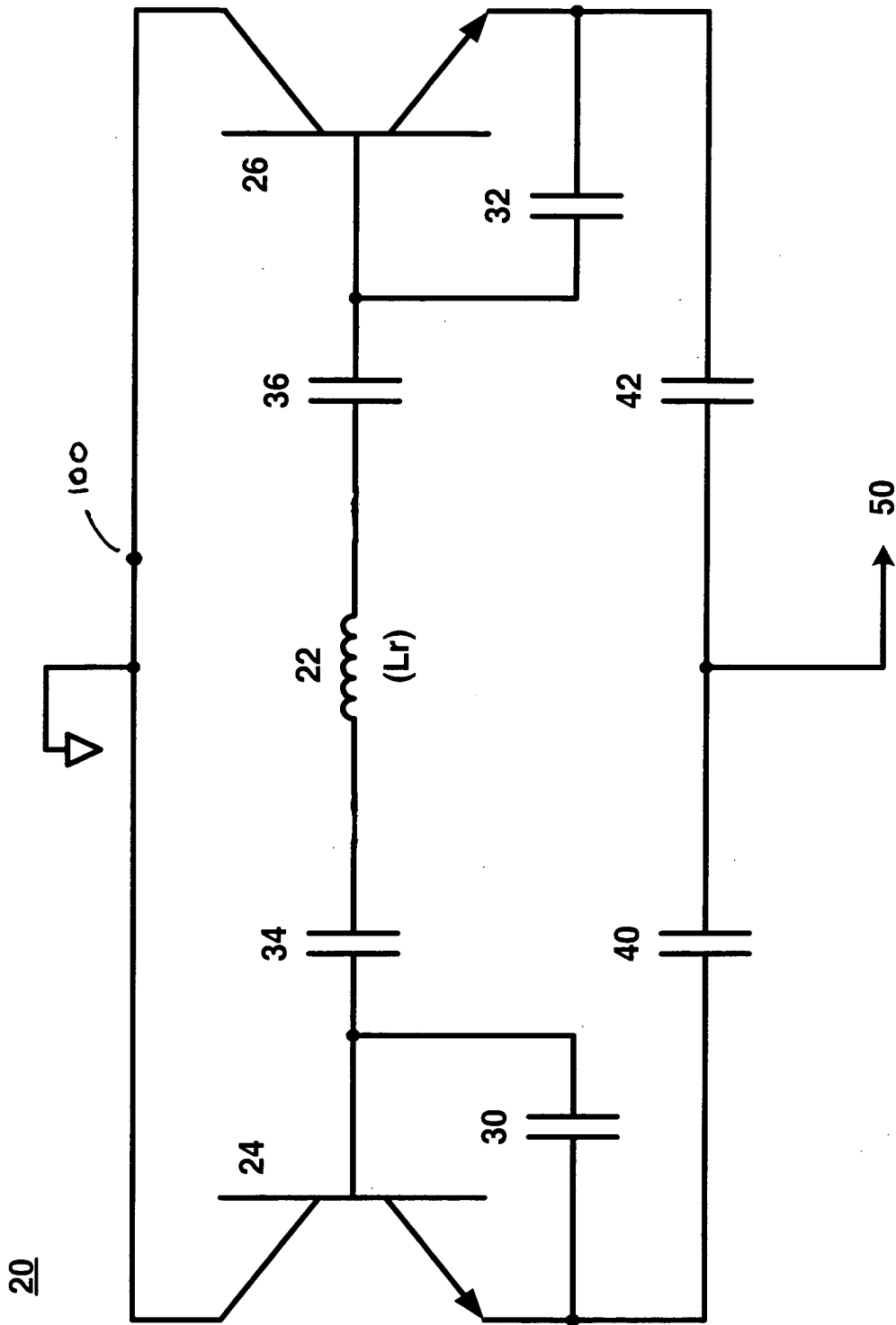


FIG. 2

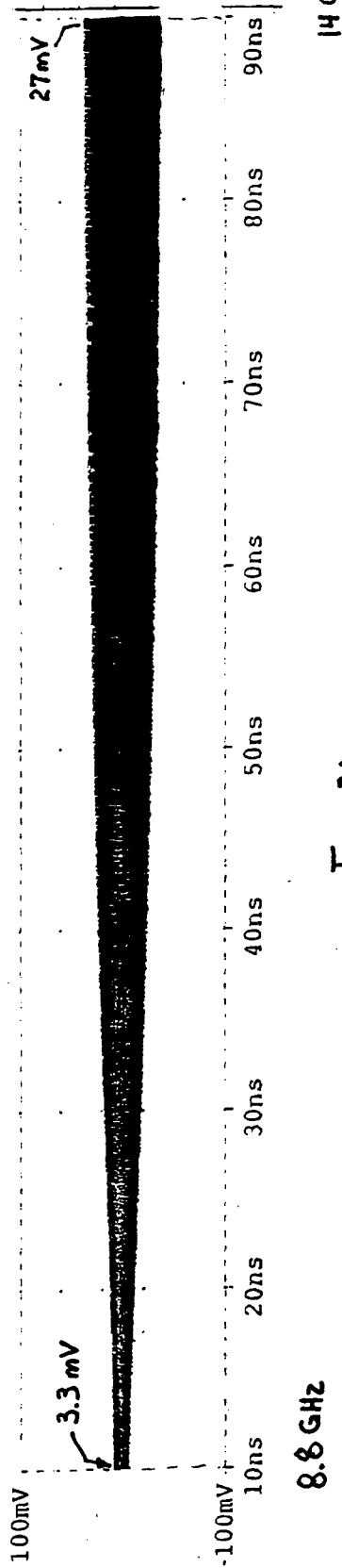


Fig. 3A

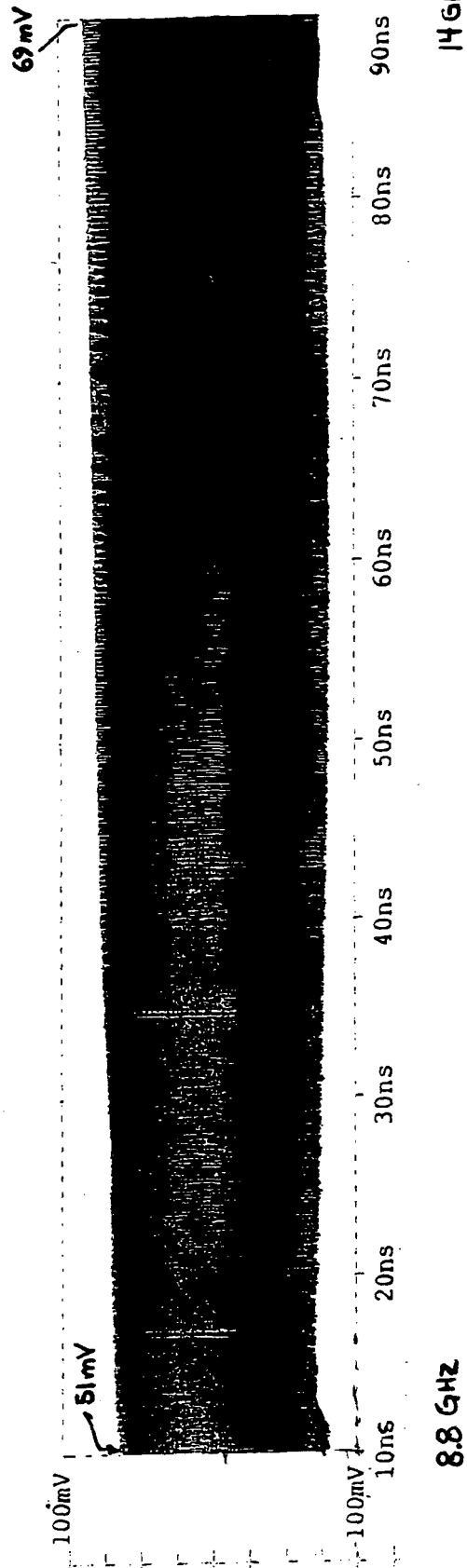


Fig. 3B

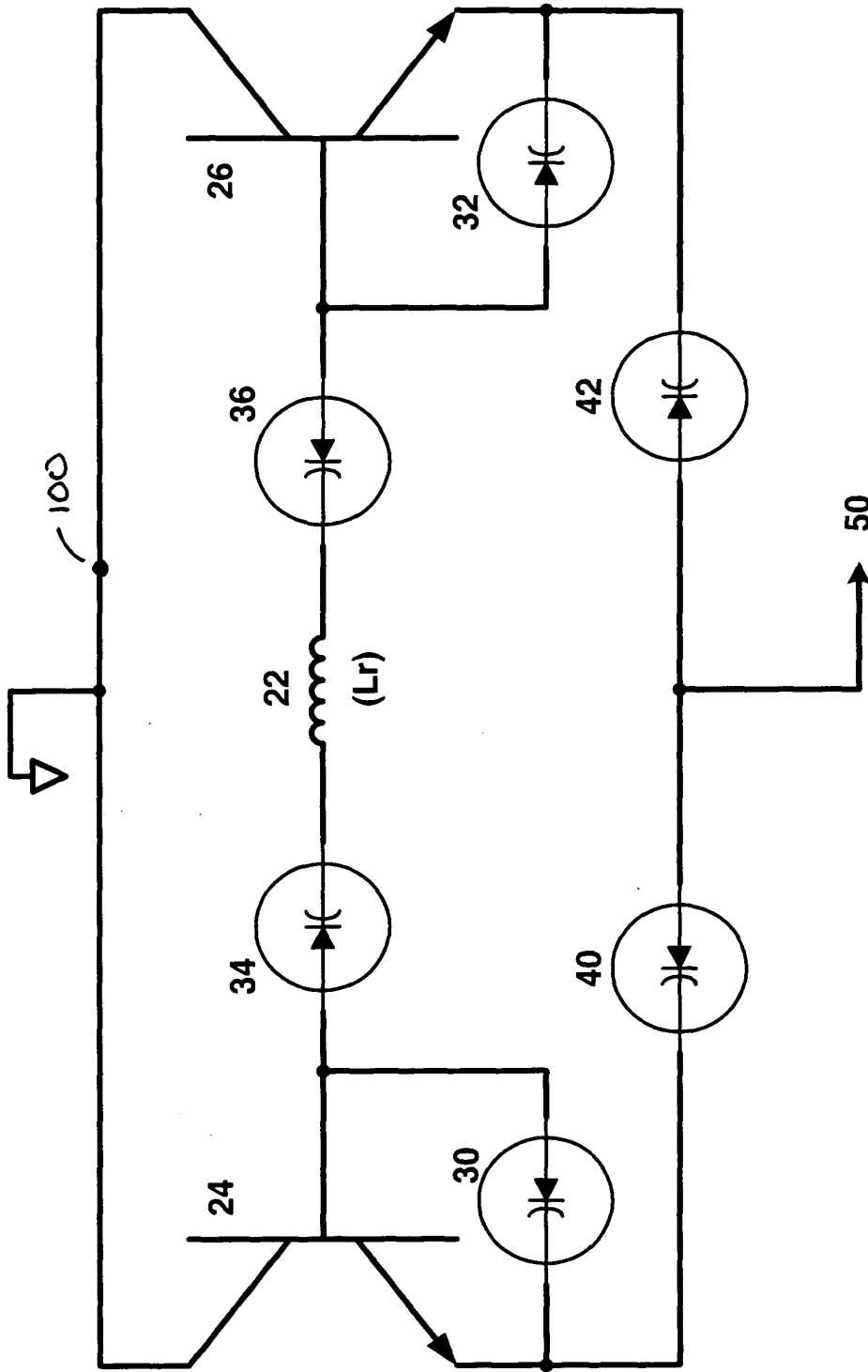


FIG. 4



FIG. 6 is a schematic diagram of a circuit 20B, which includes a transformer 22A, a capacitor 52, a capacitor 54, an inductor 22B, a capacitor 56, an inductor 22C, a capacitor 58, a capacitor 60, a capacitor 62, and a diode 102. The circuit 20B is connected to a power source 50 and a ground 102.

20B

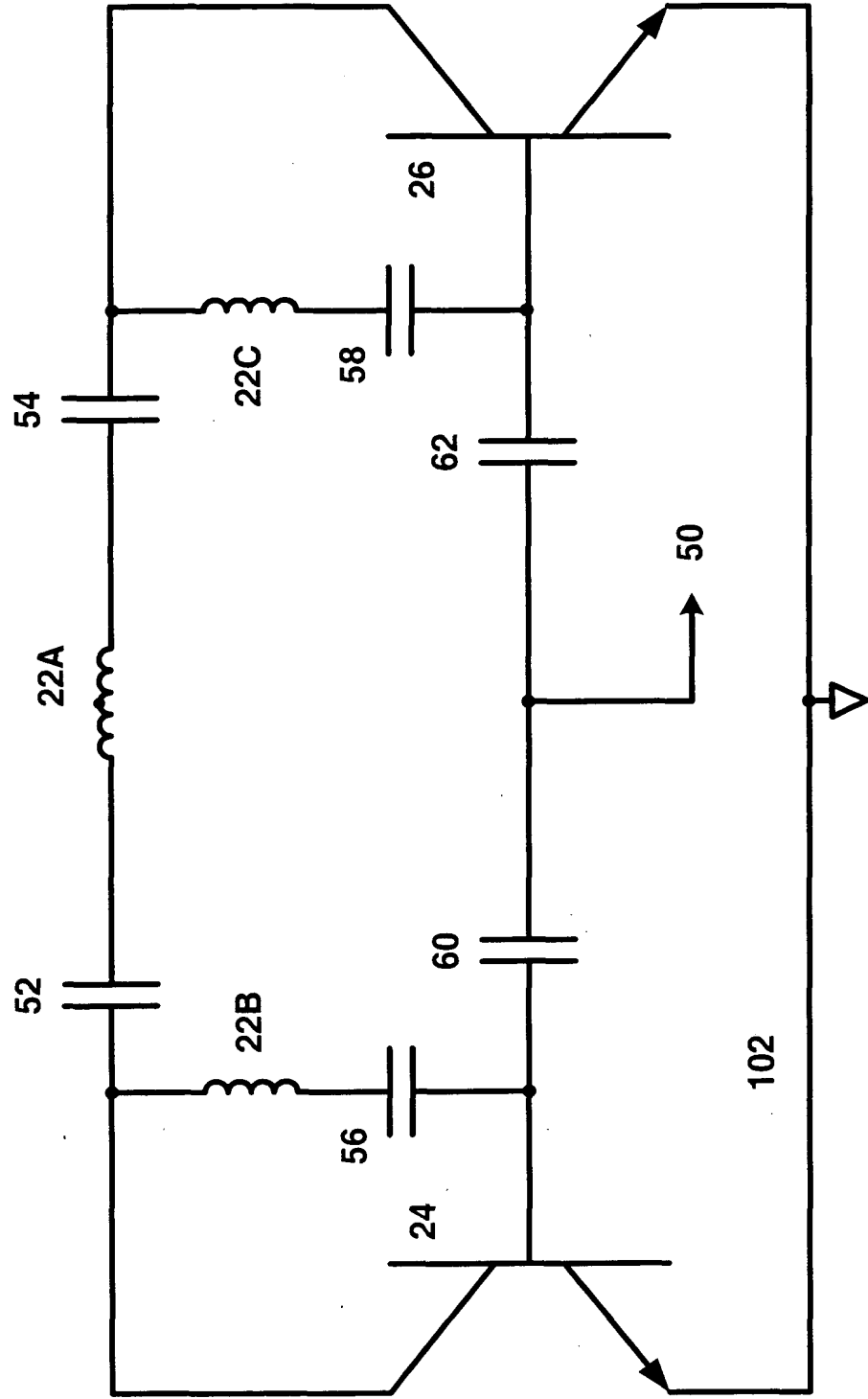


FIG. 6

FIG. 7 is a schematic diagram of a circuit in accordance with the present invention.

20C

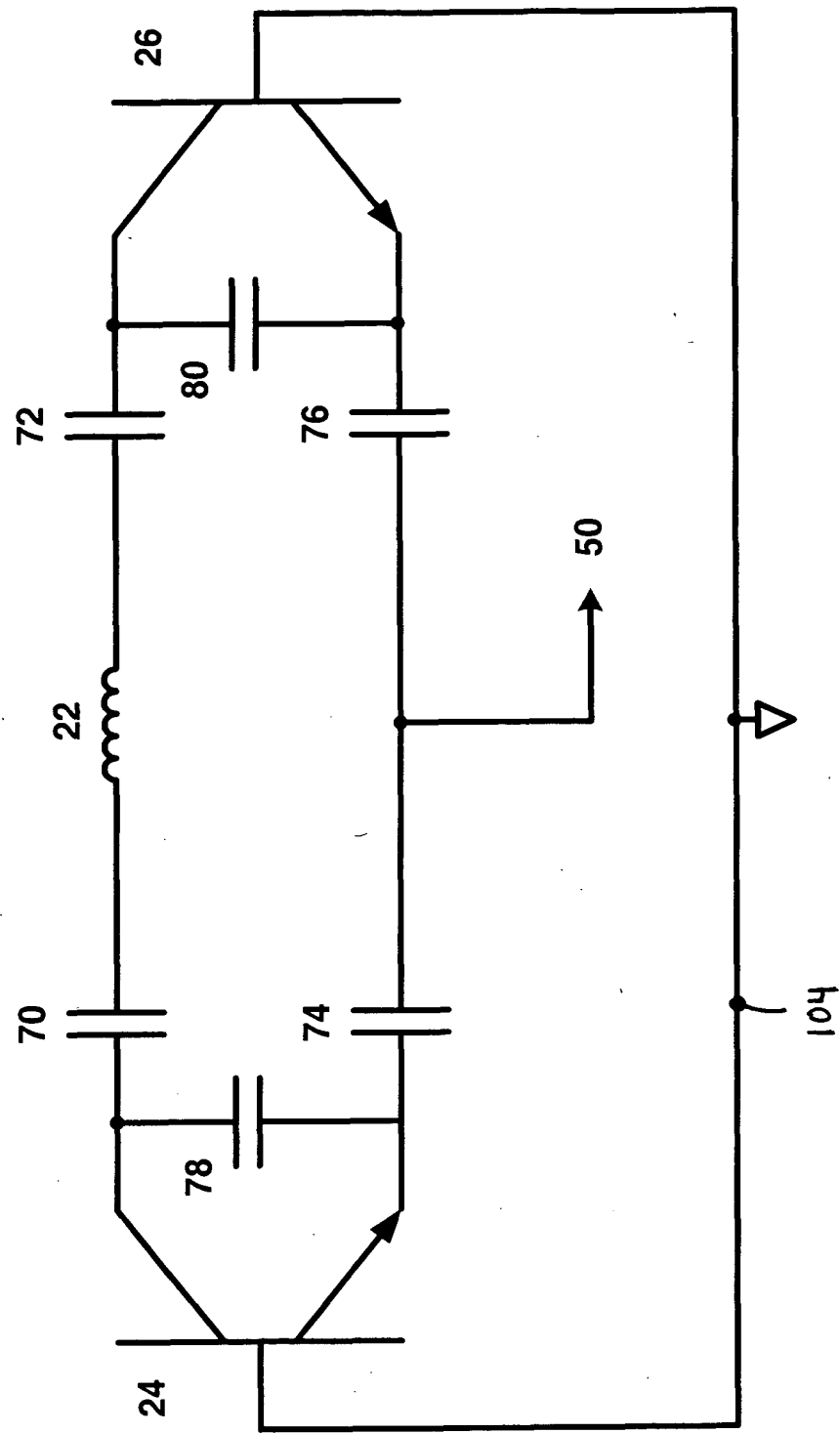


FIG. 7

20D

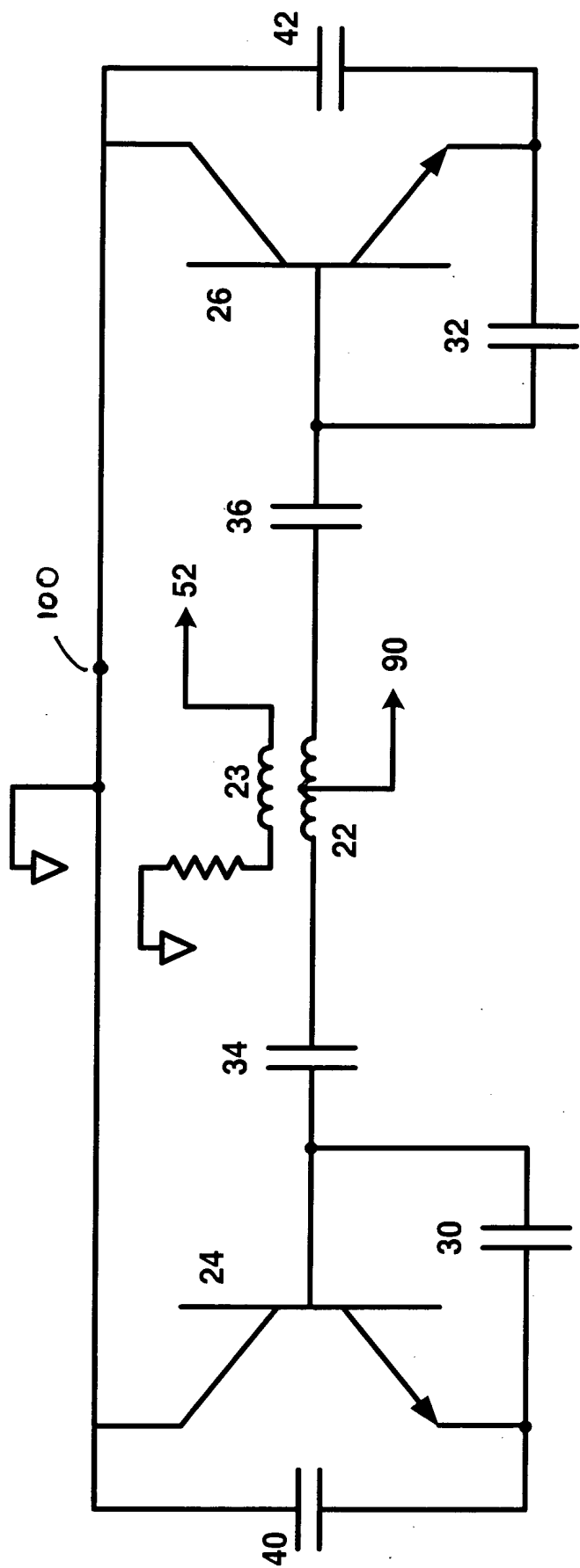


FIG. 8



FIG. 9 is a schematic diagram of a circuit 100, including a first transistor 20E, a second transistor 26, a first capacitor 30, a second capacitor 32, a first inductor 22, a second inductor 23, a first resistor 34, a second resistor 36, a first output terminal 40, a second output terminal 42, a first input terminal 50, and a second input terminal 52.

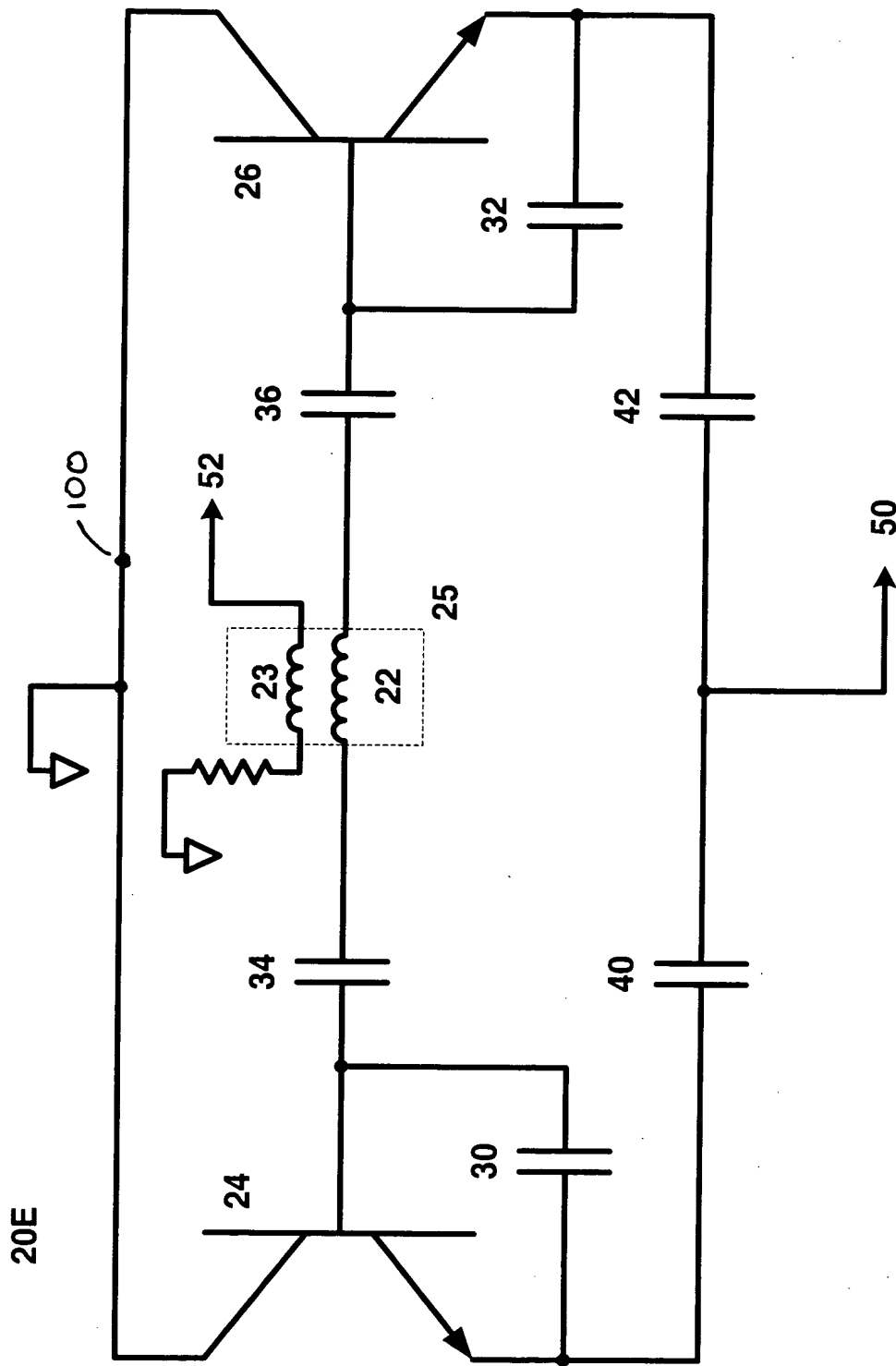


FIG. 9

20E

